



PATENT APPLICATION *IFW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Nam-Seog KIM et al.

Docket: 8836-232 (ID12245-US)

Serial No.: 10/790,262

Group Art Unit: 2818

Filed: March 1, 2004

For: SEMICONDUCTOR MEMORY DEVICE CAPABLE OF
GENERATING VARIABLE CLOCK SIGNALS ACCORDING
TO MODES OF OPERATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 2231-1450

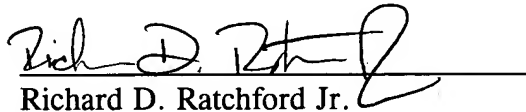
SUBMISSION OF SUPPLEMENTAL DECLARATION

Sir:

Attached herewith is a Supplemental Declaration to correct the spelling of the lead inventor's name. All the inventors have reexecuted the Supplemental Declaration in connection with the above-identified application.

Respectfully submitted,

F. CHAU & ASSOCIATES, LLC



Richard D. Ratchford Jr.

Reg. No. 53,865

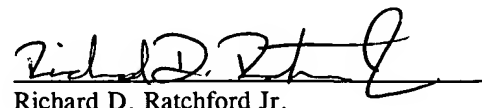
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 9, 2004.

Dated: August 9, 2004



Richard D. Ratchford Jr.